TRADET R	RANSMITTAL FORM	Application Number Filing Date First Named Inventor Art Unit Examiner Name	10/641,76 August 14 Burke, Per 2829 Wilson, C.	4, 2003 eter
Total Number of	f Pages in This Submission	10 Attorney Docket Number	03-0006	
Amendm A A Extension Express Information Certified Documer Reply to Incomple	Missing Parts/ Atte Application Attention Beply to Missing Parts Ander 37 CFR 1.52 or 1.53	Drawing(s) Licensing-related Papers Petition Petition to Convert to a Provisional Application Power of Attorney, Revocatic Change of Correspondence Terminal Disclaimer Request for Refund CD, Number of CD(s) Landscape Table on Correspondence CD, Silling	Address	After Allowance Communication to TC Appeal Communication to Board of Appeals and Interferences Appeal Communication to TC (Appeal Notice, Brief, Reply Brief) Proprietary Information Status Letter Other Enclosure(s) (please Identify below): 1. Copy of Cited Reference 2. Return Postcard
Firm Name		TURE OF APPLICANT, ATTO	RNET,	JR AGENT
Signature Printed name	LSI Logic Corporation Timothy R. Croll			
Date	12 July 05	T	Reg. No.	36,771
I hereby certify the	Cl	ERTIFICATE OF TRANSMISS eing facsimile transmitted to the USPT	O or depos	Sited with the United States Postal Service with P.O. Box 1450, Alexandria, VA 22313-1450 on

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Signature

Mark Salvatore

Date

7-13-05

This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and1.14. This collection is estimated to 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

PTO/SB/17 (12-04v2)

Approved for use through 07/31/2006. OMB 0651-0032 U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE rsons are required to respond to a collection of information unless it displays a valid OMB control number Under the Panen The crive on 100 08/2004 Complete if Known Opropriations Act. 2005 (H.R. 4818). **Application Number** 10/641,768 FEE TRANSMITTAL Filing Date August 14, 2003 For FY 2005 First Named Inventor Burke, Peter **Examiner Name** Wilson, C. Applicant claims small entity status. See 37 CFR 1.27 Art Unit 2829 TOTAL AMOUNT OF PAYMENT (\$) 180.00 Attorney Docket No. 03-0006 **METHOD OF PAYMENT** (check all that apply) Check Credit Card Money Order None Other (please identify): Deposit Account Deposit Account Number: 12-2252 Deposit Account Name: LSI Logic Corporation For the above-identified deposit account, the Director is hereby authorized to: (check all that apply) Charge fee(s) indicated below Charge fee(s) indicated below, except for the filing fee Charge any additional fee(s) or underpayments of fee(s) Credit any overpayments under 37 CFR 1.16 and 1.17 WARNING: Information on this form may become public. Credit card information should not be included on this form. Provide credit card information and authorization on PTO-2038. **FEE CALCULATION** 1. BASIC FILING, SEARCH, AND EXAMINATION FEES **FILING FEES EXAMINATION FEES** SEARCH FEES **Small Entity Small Entity** Small Entity **Application Type** Fee (\$) Fees Paid (\$) Fee (\$) Fee (\$) Fee (\$) Fee (\$) Fee (\$) Utility 300 150 500 250 200 100 Design 200 100 100 130 50 65 Plant 200 100 300 160 150 80 Reissue 300 150 500 250 600 300 200 Provisional 100 0 0 0 0 2. EXCESS CLAIM FEES **Small Entity** Fee (\$) Fee Description Fee (\$) 50 Each claim over 20 (including Reissues) 25 200 100 Each independent claim over 3 (including Reissues) Multiple dependent claims 360 180 **Total Claims Extra Claims Multiple Dependent Claims** Fee (\$) Fee Paid (\$) - 20 or HP = Fee (\$) Fee Paid (\$) HP = highest number of total claims paid for, if greater than 20. Indep. Claims **Extra Claims** Fee (\$) Fee Paid (\$) - 3 or HP = HP = highest number of independent claims paid for, if greater than 3. 3. APPLICATION SIZE FEE If the specification and drawings exceed 100 sheets of paper (excluding electronically filed sequence or computer listings under 37 CFR 1.52(e)), the application size fee due is \$250 (\$125 for small entity) for each additional 50 sheets or fraction thereof. See 35 U.S.C. 41(a)(1)(G) and 37 CFR 1.16(s). **Total Sheets** Extra Sheets Number of each additional 50 or fraction thereof Fee Paid (\$) / 50 = (round up to a whole number) x 4. OTHER FEE(S) Fees Paid (\$) Non-English Specification, \$130 fee (no small entity discount) Other (e.g., late filing surcharge): IDS Filing 180

SUBMITTED BY			
Signature	LIGKUN	Registration No. (Attorney/Agent) 36,771	Telephone (408) 433-7625
Name (Print/Type)	Timothy R. Croll		Date 17 July 05

This collection of information is required by 37 CFR 1.136. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 30 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Peter Burke et. al.

Serial No.:

10/641,768

Filed:

August 14, 2003

For:

Method and Structure for Creating

Ultra Low Resistance Damascene

Copper Wiring

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Group Art Unit:

2829

Examiner:

Wilson, C.

Atty Docket:

LSI1P219 / 03-0006

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the date below:

Mark Salvatore

July 13,2005

Date

Signature

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

The references listed in the attached form, copies of which are attached, may be material to examination of above-identified application. Applicants submit these references in compliance with their duty of disclosure pursuant to 37 CFR 1.56 and 1.97.

It is requested that the information disclosed herein be made of record in the application.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

If it is determined that any additional fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 12-2252.

LSI Logic Corporation 1621 Barber Lane, MS D-106 Milipitas, CA 95035 408-433-7475

Date: Duly 05

Respectfully submitted,

Timothy Croll

Reg. No. 36,771

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

of

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Complete if Known		
Application Number	10/641,768	
Filing Date	August-14, 2003	
First Named Inventor	Peter Burke	
Group Art Unit	2829	
Examiner Name	C. Wilson	
Attorney Docket No.	LSI1P219 / 03-0006	

ON 18 JULY STREET

Sheet

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials	Cite No.	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, caalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where	Т
		"Dual-Damascene: Overcoming Process Issues", DeJule, Semiconductor International, Reed Electronics Group, June 01, 2000	

Examiner		Date	
signature	<u> </u>	considered	